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State-of-the-Art Lab-on-a-Chip Technology in Japan

Guest Editors:

Prof. Dr. Manabu Tokeshi

Division of Applied Chemistry, Faculty of Engineering, Hokkaido University, Kita 13 Nishi 8, Kitaku, Sapporo 060-8628, Japan

Prof. Dr. Takehiko Kitamori

Department of Applied Chemistry, School of Engineering, The University of Tokyo, 7-3-1, Hongo, Bunkyo, Tokyo 113-8656, Japan

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Message from the Guest Editors

Dear Colleagues,

Japanese researchers have made great contributions to Lab-on-a-Chip communities since 1990, when the concept of $\mu\text{-TAS}$ was proposed. Lab-on-a-Chip technology from Japan always had a significant impact on the mainstream of Lab-on-a-Chip and $\mu\text{-TAS}$ research. We expect that Japanese researchers will contribute continuously to this field. This Special Issue aims to provide a comprehensive overview of the state-of-the-art Lab-on-a-Chip technology in Japan. We invite research articles that will consolidate our understanding of the state-of-the-art in this area. The Special Issue will publish full research, review, and highly-rated manuscripts addressing the abovementioned topic.

Prof. Dr. Manabu Tokeshi Prof. Dr. Takehiko Kitamori *Guest Editors*













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Editor-in-Chief

Message from the Editor-in-Chief

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