Message from the Guest Editors

Dear Colleagues,

Subthreshold microelectronic circuits are gaining momentum for ultra-low-power applications but their maturity remains low when compared to conventional above-threshold designs. Therefore, this special issue of JLPEA is intended to focus on the advances in subthreshold microelectronics with extended versions of papers presented from the 2011 Subthreshold Microelectronics Conference (SubVt 2011) held in Lexington, MA, USA on 26-27 September 2011. However, other works related to ultra-low-power microelectronics not presented at SubVt 2011 will also be considered.

Dr. David Bol
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Guest Editors

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