



Ultra-low Power Embedded Systems

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Deadline for manuscript
submissions:

closed (31 December 2018)

Message from the Guest Editors

The future of the computing continuum is linking low-end embedded systems with the entire Hw/Sw stack, encompassing also high-performance systems. Many possible solutions to the issue of power consumption can be shared across these abstraction layers. Embedded systems play a key role to the spread of intelligence in everyday applications and one of the critical success factor is to reduce the power and energy consumption, dramatically. Such goal can be achieved by tackling the problem from different angles, including the design of the hardware, software, sensing and communication components, as well as by considering in a vertical way the development of the application, as for example required for the next generation of IoT solutions. Smart, run-time adaptable and ultra low power edge devices are the only viable option that can meet these demands.

Within the above scenario, this special issue focuses on the latest developments in the field of designing ultra low power embedded systems. The call reflects a broad range of research topics: from theoretical aspects down to best practices and experiments with existing and novel platforms and applications.





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Message from the Editor-in-Chief

Journal of Low Power Electronics and Applications (ISSN 2079-9268) is an open access journal which provides an advanced forum for the studies of electronics for low power applications. A special emphasize is made on ultralow power bio-medical applications. It publishes reviews, regular research papers and short communications.

Our aim is to encourage scientists to publish their experimental and theoretical results in as much detail as possible. There is no restriction on the length of the papers. Full experimental and/or methodical details must be provided.

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