



Low-Power Electronic Circuits for Monolithic Smart Wireless Sensors

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submissions:

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Message from the Guest Editors

Recent advancements in sensor and integrated circuit technologies have facilitated widespread use of smart wireless sensor instruments in various applications. With continuous scaling of CMOS process technologies, a high degree of miniaturization has been achieved, which has led to the realization of complex analytical systems. The purpose of this Special Issue is to address research activities in the design of transducers and the associated electronics, including wireless telemetry required for achieving smart wireless monolithic sensor instruments:

- Low-power circuit design methodologies
- Transducer design for monolithic sensors
- Energy harvesting techniques for battery-less sensor instruments
- Power and energy management circuits and systems
- Circuits techniques for energy-efficient wireless communication
- Wireless smart sensor architecture and system-level design methodologies
- Low-power, intelligent and adaptive sensor signal processing for wireless sensors





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Message from the Editor-in-Chief

Journal of Low Power Electronics and Applications (ISSN 2079-9268) is an open access journal which provides an advanced forum for the studies of electronics for low power applications. A special emphasize is made on ultralow power bio-medical applications. It publishes reviews, regular research papers and short communications.

Our aim is to encourage scientists to publish their experimental and theoretical results in as much detail as possible. There is no restriction on the length of the papers. Full experimental and/or methodical details must be provided.

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