Hardware Security and Trust

Guest Editor:

Dr. Paolo Maistri
TIMA Laboratory, CNRS, 38031 Grenoble, France

Deadline for manuscript submissions:
31 May 2024

Message from the Guest Editor

This Special Issue seeks novel contributions to improve the current state-of-the-art literature on methodologies, tools, and results on architectures, experimental attacks, and countermeasures for embedded systems in the field of hardware security and trust. Topics of interest include but are not limited to the following:

Embedded implementation of cryptographic algorithms;
Physical attacks against embedded implementations and related countermeasures;
Security of test infrastructures;
Hardware Trojans and detection techniques;
Hardware security primitives;
Secure processors and architectures;
Microarchitectural attacks: characterization, exploitation, protections;
Post-quantum cryptographic implementations;
Lightweight cryptographic implementations;
Secure implementation in constrained environments.

Special Issue

Editor-in-Chief

Prof. Dr. Willy Susilo
School of Computer Science and Software Engineering, University of Wollongong, Northfields Avenue, Wollongong, NSW 2522, Australia

Message from the Editor-in-Chief

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