



Safe and Secure Embedded Systems

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Deadline for manuscript
submissions:

closed (1 January 2019)

Message from the Guest Editors

This Special Issue solicits novel research in topics that include, but are not limited to:

- modeling of safety and security concerns in embedded systems
- interdependencies between safety and security in embedded systems
- integrated approaches safety and security
- security in safety-critical systems
- run-time mechanisms in hardware and software to support safety and security
- safety and security in parallel and/or heterogeneous computing platforms
- methods, tools, and techniques for modeling, design, and verification of safe and secure embedded systems
- implications of regulation and certification on safety and security in embedded systems
- thread and hazard analysis of hardware and software components
- fault tolerant designs for safe and secure embedded systems
- hardware and software co-design and specialization for safe and secure embedded systems

Welcome to contribute!





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Message from the Editor-in-Chief

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