



Hardware in the Loop, Real-Time Simulation and Digital Control of Power Electronics and Drives, 2nd Edition

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Deadline for manuscript
submissions:
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Message from the Guest Editors

Dear Colleagues,

The main objective of this Special Issue is to provide experts in the field of power electronics and drives the opportunity to present their research and development efforts in the field of hardware-in-the-loop, real-time simulations, digital control, and functional safety of power electronics and drives, with an emphasis on practical applications. Therefore, researchers who are involved in the above mentioned research topics are invited to submit their manuscripts and to contribute their expertise to this Special Issue.

Keywords

- power converters
- motor drives
- digital controllers
- microcontroller-based applications
- FPGA-based controllers
- system-on-chip-based controllers
- software-in-the-loop
- hardware-in-the-loop
- functional safety of the power electronics
- fault-tolerant techniques
- microcomputer-based real-time simulations
- FPGA-based real-time simulations





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Message from the Editor-in-Chief

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