



Hardware and Architecture

Guest Editor:

Prof. Dr. Michael Hübner

Computer Engineering,
Brandenburg University of
Technology Cottbus-
Senftenberg, Universitätsplatz 1,
01968 Senftenberg, Germany

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Message from the Guest Editor

Novel trends in Cyber physical Systems, the Internet of things, and also neuronal networks and machine learning, need new hardware and architecture. The goal is to reduce energy consumption more, and to simultaneously increase performance. This Special Issue will cover novel trends, research, and the development of hardware and architecture, and contributes to the community with different views and solutions.

Keywords:

- Embedded system
- Low power
- Reconfigurable computing

Prof. Dr. Michael Huebner
Guest Editor





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Editor-in-Chief

Prof. Dr. Flavio Canavero

Department of Electronics and
Telecommunications,
Politecnico di Torino, 10129
Torino, Italy

Message from the Editor-in-Chief

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Electronics Editorial Office
MDPI, Grosspeteranlage 5
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