



Article Observer-Based Sliding Mode Control to Improve Stability of Three-Phase LCL-Filtered Grid-Connected VSIs

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Abstract: Grid-connected voltage source inverters (VSIs) with LCL filters have been widely used for distributed generation systems (DGs). Various control methods have been studied to achieve good performance. Among them, sliding mode control has been applied to LCL-filtered grid-connected VSIs for its fast, dynamic response and strong robustness. However, LCL networks can easily cause instability problems under weak grid conditions such as grid impedance variation. At the same time, the stability design of sliding mode control applied for LCL-filtered grid-connected inverters are important, but they lack detailed parameters design in recent papers. In this paper, a design of observer-based sliding mode control to improve the stability of three-phase LCL-filtered grid-connected VSIs was proposed. The theoretical stability analysis was developed to consider the effect of the system discretization and grid impedance variations. Finally, a 3-kW, 110-V, 50-Hz experimental setup has been built to demonstrate the validation of the proposed method.

Keywords: LCL filter; grid-connected inverters; observer; sliding mode control; stability analysis; robustness

1. Introduction

Due to energy demand and environmental problems, a grid-connected voltage source inverter (VSI) has been widely used in new energy power generation, especially in distributed generation systems (DGs) [1–3]. In order to inject high quality current into the power grid, a filter is needed to connect the inverter and the power grid to attenuate the high frequency harmonics caused by pulse width modulation (PWM) [4,5].

Among these various filters, the third-order LCL filter is a good choice in which to attain better attenuation ability with lower inductance compared with L and LC filters. However, an LCL filter has an inherent resonance problem, which causes system instability and brings complexity to the current regulator [6,7]. In general, in order to suppress the resonance, active and passive damping methods are frequently adopted to an LCL-filtered converter [8–11]. A passive damping method is usually realized by adding physical resistors to the filter, which will certainly result in power loss. An active damping method is usually adopted to construct a virtual resistor by using state feedback variables, in which one or more additional sensors are needed in the implementation. In addition, several control methods are also proposed without damping methods, which strongly depends on the LCL filter design and the sampling frequency [9]. However, grid-connected inverters are connected at the point-of-common-coupling (PCC) and the equivalent impedance of the power grid would change in a wide range, so that the resonance frequency of the system would probably vary, which can easily

lead to instability in the digital implementation in DGs. Hence, the controller design becomes very important when facing increasingly complex and rough operation conditions [11].

Conventionally, current loop with a proportional integral (PI) controller is used for grid-connected converters. However, the poor reference tracking and stability issues are the main drawbacks for this type of controller. Instead of a PI controller, the proportional resonant (PR) controller has been widely used for good reference tracking and harmonic compensation under the grid disturbance [12,13]. In recent years, compared with these traditional linear control methods, various non-linear control methods, such as passivity-based control, sliding mode control (SMC), repetitive control, predictive control and deadbeat control, have been rapidly applied to LCL-filtered grid-connected VSIs [14–26]. Non-linear control methods, SMC has been widely studied in grid-connected converters due to its fast, dynamic response, strong robustness and good regulation properties [20–26].

Normally, for LCL-filtered grid-connected VSIs the sliding surface is formed based on the linear combination of various variables such as the inverter current error, the capacitor voltage error, the grid current error and derivatives of these variables. Different sliding mode functions could lead to different control effects in a number of required sensors. The SMC method introduced in [20] requires three sensors for measuring the grid and inverter and capacitor currents. To reduce the number of sensors for LCL-filtered inverters, the sliding mode controller was combined to the Kalman filter to estimate the actual variables needed in the sliding surface function formation [22,23]. As for the Kalman filter, the main merit is the good performance of the noise immunity, but it has an issue of high computational burden. As to the steady state error problem of the grid current, PR with multiple resonant terms controller has been used in the SMC [20,21]. The authors of Reference [25] introduced a virtual resistor in the converter model with sliding mode control to achieve a high robustness against internal and external disturbance. However, there are few papers which have provided a detailed design procedure of the sliding mode controller parameters of the LCL-filtered grid-connected inverter.

In this paper, an observer-based sliding mode control method is proposed to improve the stability of the three-phase LCL-filtered grid-connected VSI. In order to further reduce the cost and to facilitate design, a discrete state observer was constructed by only sampling two state variables, the grid-side current and the PCC voltage. Compared with the Kalman filter, as a similar senseless method to estimate the state vectors, a state observer is much simpler to implement and has been successfully applied to LCL-filtered grid-connected [27–30]. Because of the separation principle of a state observer, the design of the sliding mode controller and discrete state observer were separated. As to the current control loop, the sliding mode controller was built to regulate the inverter-side current, so the sliding surface is only based on the error of the inverter-side current in which the control law is much simpler and easy to implement. However, due to the resonance problem of an LCL filter, the stability range of the system will become smaller and the bandwidth cannot be improved. Thus, a modified sliding mode control law with active damping ability is proposed to improve the stability of the system and to reduce the sensitivity to grid inductance variations. Furthermore, the detailed parameters design of the proposed method has been described in consideration of the discretization time delay.

This paper is organized as following: Section 2 introduces the modeling of a three-phase grid-connected VSI with an LCL filter in $\alpha\beta$ reference frame; Section 3 analyzes the proposed control approach and the design procedures; then, Section 4 presents experimental results of the whole system to verify the effectiveness and the theoretical considerations of the proposed method. Finally, some conclusions are derived in Section 5.

2. Modeling of a Three-Phase Grid-Connected VSI with an LCL Filter in $\alpha\beta$ -Frame

Figure 1 shows the topology of a three-phase grid-connected VSI with an LCL filter and the basic control scheme of the proposed strategy. The LCL filter consists of an inverter-side inductor L_1 , a filter capacitor *C*, and a grid-side inductor L_2 . r_1 and r_2 are the parasitic resistances of inductors L_1 and L_2 . Considering that most of the inverters are connected to the grid in parallel through the PCC in the

DGs, here the impedance of the grid between the voltage v_{pcc} at the PCC and the grid voltage v_g is equivalent to a pure inductor L_g . The phase-locked loop (PLL) is used to generate the current reference i_1^* synchronized with the PCC voltage. It can be seen from the Figure 1 that only two state vectors, the grid-side current i_2 and the PCC voltage v_{pcc} , are sensed in this system. The inverter-side current \hat{i}_1 and the capacitor voltage \hat{v}_c obtained by state observer are used for SMC controller and realized in the stationary $\alpha\beta$ -frame. $u_{i\alpha\beta}$ is the control signal synthesized by the SMC controller under the stationary $\alpha\beta$ -frame. The values of the system parameters chosen for the theoretical analysis and experiments are given in Table 1.



Figure 1. Three-phase grid connected VSI with LCL filter and controller.

| Symbol | Value |
|--|--------------|
| DC link voltage U_{dc} | 350 V |
| Grid voltage v_g | 110 V(RMS) |
| Sampling frequency f_s | 12 kHz |
| Switching frequency <i>f</i> _{sw} | 12 kHz |
| Inverter-side inductance L_1 | 1.2 mH |
| Filter capacitance C | 6 µF |
| Grid-side inductance L_2 | 1.2 mH |
| Inductor resistors r_1, r_2 | 0.2 Ω, 0.2 Ω |

Table 1. System parameters.

The operation of the three-phase system can be described by the following equations:

$$L_1 \frac{di_1}{dt} = u_i - i_1 r_1 - v_c \tag{1}$$

$$C\frac{dv_c}{dt} = i_1 - i_2 \tag{2}$$

$$L_2 \frac{di_2}{dt} = v_c - i_2 r_2 - v_{pcc}$$
(3)

where the state vectors used for sliding mode control are defined as: the inverter-side currents: $i_1 = \begin{bmatrix} i_{1a} & i_{1b} & i_{1c} \end{bmatrix}^T$, the capacitor voltages $v_c = \begin{bmatrix} v_{ca} & v_{cb} & v_{cc} \end{bmatrix}^T$, the grid-side currents $i_2 = \begin{bmatrix} i_{2a} & i_{2b} & i_{2c} \end{bmatrix}^T$ and the control signals vector $u_i = \begin{bmatrix} u_{ia} & u_{ib} & u_{ic} \end{bmatrix}^T$. In order to facilitate the design of control and avoid the coupling problem in *dq*-frame, the operation of the system is converted to $\alpha\beta$ -frame, shown in Equations (4)–(6):

$$\begin{cases} L_1 \frac{di_{1\alpha}}{dt} = u_{i\alpha} - i_{1\alpha}r_1 - v_{c\alpha} \\ L_1 \frac{di_{1\beta}}{dt} = u_{i\beta} - i_{1\beta}r_1 - v_{c\beta} \end{cases}$$
(4)

$$\begin{cases} C\frac{dv_{c\alpha}}{dt} = i_{1\alpha} - i_{2\alpha} \\ C\frac{dv_{c\beta}}{dt} = i_{1\beta} - i_{2\beta} \end{cases}$$
(5)

$$\begin{cases} L_2 \frac{di_{2\alpha}}{dt} = v_{c\alpha} - i_{2\alpha}r_2 - v_{pcc\alpha} \\ L_2 \frac{di_{2\beta}}{dt} = v_{c\beta} - i_{2\beta}r_2 - v_{pcc\beta} \end{cases}$$
(6)

Then the state-space model of the system can be described by Equations (7) and (8):

$$\dot{x} = Ax + Bu + D \tag{7}$$

$$A = \begin{bmatrix} -\frac{r_1}{L_1} & 0 & -\frac{1}{L_1} & 0 & 0 & 0\\ 0 & -\frac{r_1}{L_1} & 0 & -\frac{1}{L_1} & 0 & 0\\ \frac{1}{C} & 0 & 0 & 0 & -\frac{1}{C} & 0\\ 0 & \frac{1}{C} & 0 & 0 & 0 & -\frac{1}{C}\\ 0 & 0 & \frac{1}{L_2} & 0 & -\frac{r_2}{L_2} & 0\\ 0 & 0 & 0 & \frac{1}{L_2} & 0 & -\frac{r_2}{L_2} \end{bmatrix}, B = \begin{bmatrix} \frac{1}{L_1} & 0\\ 0 & \frac{1}{L_1} \\ 0 & 0\\ 0 & 0 \\ 0 & 0 \\ 0 & 0 \end{bmatrix}, D = \begin{bmatrix} 0\\ 0\\ 0\\ 0\\ -\frac{v_{pcc\alpha}}{L_2} \\ -\frac{v_{pcc\beta}}{L_2} \end{bmatrix}$$
where: $x = \begin{bmatrix} i_{1\alpha} & i_{1\beta} & v_{c\alpha} & v_{c\beta} & i_{2\alpha} & i_{2\beta} \end{bmatrix}^T, u = \begin{bmatrix} u_{i\alpha}u_{i\beta} \end{bmatrix}^T.$

$$(8)$$

3. Proposed Observer-Based Sliding Mode Control System

Figure 1 shows the control scheme of the whole system. Compared to a traditional sliding mode control method, the proposed control algorithm is based on a state observer and active damping method to improve the robustness against the grid inductance and system parameters variations. The grid-side current vector $i_2 = \begin{bmatrix} i_{2a} & i_{2b} & i_{2c} \end{bmatrix}^T$ is sensed for the input of the state observer. The PCC voltage vector $v_{pcc} = \begin{bmatrix} v_{pcca} & v_{pccb} & v_{pccc} \end{bmatrix}^T$ is sensed for PLL and the input of the state observer. The PCC Then the observed inverter-side current vector $\hat{i}_1 = \begin{bmatrix} \hat{i}_{1\alpha} & \hat{i}_{1\beta} \end{bmatrix}^T$, the observed capacitor voltage vector $\hat{v}_c = \begin{bmatrix} \hat{v}_{c\alpha} & \hat{v}_{c\beta} \end{bmatrix}^T$ and the reference inverter current vector $i_1^* = \begin{bmatrix} i_{1\alpha}^* & i_{1\beta}^* \end{bmatrix}^T$ are used in the modified SMC controller. Here are the detailed design procedures and stability analysis.

3.1. Basic SMC Controller

This section introduces the design of the basic SMC controller. Figure 2 shows the control scheme in the stationary $\alpha\beta$ -frame. L_1' and r_1' are the measured values of the inverter-side inductor and the parasitic resistance, respectively. As a special non-linear control, the sliding mode control is based on Lyapunov stability analysis. The sliding surface was constructed by its desired goal, and then the control law was obtained by the Lyapunov stability analysis method, which forces the system to run to the desired trajectory. In this LCL-filtered grid-connected inverter system, the aim of the controller is to inject high-quality current into the grid in which the six state variables $x = \begin{bmatrix} i_{1\alpha} & i_{1\beta} & v_{c\alpha} & v_{c\beta} & i_{2\alpha} & i_{2\beta} \end{bmatrix}^T$ are depicted in Equation (7) are the same as the desired reference state variables. Note that there is a delay problem in the process of digital implementation, which will greatly affect the stability of the system and needs to be considered.



Figure 2. Basic SMC control scheme diagram of grid connected VSI in $\alpha\beta$ -frame.

(1) Select the sliding surface.

As a first step, the sliding surface vectors are defined to control the inverter currents, as shown in Equations (9) and (10):

$$s = \begin{bmatrix} s_{\alpha} & s_{\beta} \end{bmatrix}^T \tag{9}$$

$$\begin{cases} s_{\alpha} = i_{1\alpha}^{*} - i_{1\alpha} \\ s_{\beta} = i_{1\beta}^{*} - i_{1\beta} \end{cases} \begin{cases} \frac{ds_{\alpha}}{dt} = \frac{dt_{1\alpha}}{dt} - \frac{i_{1\alpha}}{dt} \\ \frac{ds_{\beta}}{dt} = \frac{dt_{1\beta}}{dt} - \frac{i_{1\beta}}{dt} \end{cases}$$
(10)

(2) Choose the reaching law.

The reaching law method is an effective method to suppress the chattering of the system, which determines the dynamic characteristics of the system. Here, the exponential reaching law based on saturation function is selected by Equations (11) and (12):

$$\begin{cases} \frac{ds_{\alpha}}{dt} = -k_p s_{\alpha} - \varepsilon sat(s_{\alpha}) \\ \frac{ds_{\beta}}{dt} = -k_p s_{\beta} - \varepsilon sat(s_{\beta}) \end{cases}$$
(11)

$$sat(s) = \begin{cases} 1 & s > \Delta \\ k_{sat}s & |s| \le \Delta \ k_{sat} = \frac{1}{\Delta} \\ -1 & s < -\Delta \end{cases}$$
(12)

Note that this reaching law can increase the gain of k_p and decrease the gain of non-linear links ε . The parameter selecting of ε and k_{sat} is based on engineering experience. The ε is very small, and the sat function can be neglected without affecting the results.

(3) Get the control law.

Substituting Equation (7) into Equation (11) yields:

$$\begin{aligned}
 (u_{i_{\alpha}} = L_1 k_p s_{\alpha} + L_1 \varepsilon sat(s_{\alpha}) + L_1 \frac{dt_{1\alpha}^*}{dt} + r_1 i_{1\alpha}^* - v_{c\alpha} \\
 u_{i_{\beta}} = L_1 k_p s_{\beta} + L_1 \varepsilon sat(s_{\beta}) + L_1 \frac{dt_{1\beta}^*}{dt} + r_1 i_{1\beta}^* - v_{c\beta}
\end{aligned}$$
(13)

In order to eliminate the inverter current tracking error and suppress the harmonics distortion, the proportional gain of the reaching law is replaced with the PR controller. The transfer function of PR controller is given by Equation (14):

$$G_c(s) = k_p + \frac{2k_r\omega_i s}{s^2 + 2\omega_i s + \omega_o^2}$$
(14)

where k_p is the proportional gain, and k_r is the resonant gains, ω_i and ω_0 is the cutoff angular frequency and the fundamental angular frequency. It should be noted that only the proportional gain of $G_c(s)$, k_p , is considered in the later analysis, since the resonant parts of Equation (14) are designed to improve steady-state tracking [24]. It has little effect in resonance damping, if the same proportional gain is used. When considering the delay of digital implementation, it needs to be reflected in the control structure diagram. T_s is the sampling period. $G_{zoh}(s)$ is the zero-order hold given by Equation (15):

$$G_{zoh}(s) = \frac{1 - e^{-T_s \cdot s}}{s}$$
 (15)

Thus, total delay $G_d(s)$ is shown in Equation (16):

$$G_d(s) = \frac{1}{T_s} e^{-T_s \cdot s} G_{zoh}(s) \approx e^{-1.5T_s \cdot s}$$
(16)

As shown in Figure 2, the control structure of the system can be obtained from the above analysis. Since the designed ε is very small compared to the main loop gain, the nonlinear link is negligible in the stability analysis. The closed-loop transfer function of the whole system considering the delay is given by Equation (17):

$$G_{close}(s) = \frac{I_1(s)}{I_1^*(s)} = \frac{a_3 s^3 + a_2 s^2 + a_1 s + a_0}{b_3 s^3 + b_2 s^2 + b_1 s + b_0}$$
(17)

where:

$$a_{3} = CL_{2}L'_{1}$$

$$a_{2} = CL_{2}k_{p} + CL_{2}r'_{1} + CL'_{1}r_{2}$$

$$a_{1} = L'_{1} + Ck_{p}r_{2} + Cr_{2}r'_{1}$$

$$a_{0} = k_{p} + r'_{1}$$

$$b_{3} = CL_{1}L_{2}e^{1.5T_{s}\cdot s}$$

$$b_{2} = CL_{2}k_{p} + CL_{1}r_{2}e^{1.5T_{s}\cdot s} + CL_{2}r_{1}e^{1.5T_{s}\cdot s}$$

$$b_{1} = L_{2} + L_{1}e^{1.5T_{s}\cdot s} + L_{2}e^{1.5T_{s}\cdot s} + Ck_{p}r_{2} + Cr_{1}r_{2}e^{1.5T_{s}\cdot s}$$

$$b_{0} = k_{n} + r_{2} + r_{1}e^{1.5T_{s}\cdot s} + r_{2}e^{1.5T_{s}\cdot s}$$
(18)

The discrete closed-loop poles location of the whole system can be obtained from the closed -loop transfer function when $0.01 < k_p < 5$, as shown in Figure 3. The VSI is controlled using the conventional SMC without any damping strategy. It can be seen that the poles move out of the z-domain unit circle with increasing k_p and the critical stable point is $k_p = 2$. However, it should be noted that the resonant frequency largely depends on the grid impedance variations. In order to increase the system control bandwidth and robustness against the system parameters variations, the modified SMC and stability analysis will be proposed in the next sections.



Figure 3. Closed-loop poles locations of $G_1(s)$ when k_p varies from 0.01 to 5.

3.2. Discrete State Observer

This section introduces the design of a discrete state observer. It can see from the above control law that the system needs to detect three state variables in which nine sensors are needed. The state observer is an effective method to reduce system sensors, and it does not take up excessive computation in software computing. The inverter-side current i_1 and capacitor voltage v_c can be estimated with grid-side current i_2 and the PCC voltage v_{pcc} using a state observer.

For digital implementation, in the design of state observer, the system model shown in Equation (7) should be transformed into discrete state-space model, given in Equation (19):

$$\begin{cases} x(k+1) = A_d x(k) + B_d u(k) + D_d(k) \\ y(k) = C_c x(k) \end{cases}$$
(19)

where:

$$A_{d} = e^{AT_{s}} = \begin{bmatrix} 1 - \frac{r_{1}T_{s}}{L_{1}} & 0 & -\frac{T_{s}}{L_{1}} & 0 & 0 & 0\\ 0 & 1 - \frac{r_{1}T_{s}}{L_{1}} & 0 & -\frac{T_{s}}{L_{1}} & 0 & 0\\ \frac{T_{s}}{C} & 0 & 0 & 0 & -\frac{T_{s}}{C} & 0\\ 0 & \frac{T_{s}}{L_{2}} & 0 & 0 & 0 & -\frac{T_{s}}{C} \\ 0 & 0 & \frac{T_{s}}{L_{2}} & 0 & 1 - \frac{r_{2}T_{s}}{L_{2}} & 0\\ 0 & 0 & 0 & 0 & \frac{T_{s}}{L_{2}} & 0 & 1 - \frac{r_{2}T_{s}}{L_{2}} \\ \end{bmatrix}, B_{d} = \int_{0}^{T_{s}} e^{A\tau} d\tau B = \begin{bmatrix} \frac{T_{s}}{L_{1}} & 0\\ 0 & 0\\ 0 & 0\\ 0 & 0\\ 0 & 0\\ 0 & 0 \end{bmatrix}, D_{d} = \int_{0}^{T_{s}} e^{A\tau} d\tau D = \begin{bmatrix} 0\\ 0\\ 0\\ 0\\ 0\\ -\frac{v_{peer}}{L_{2}} \\ -\frac{v_{peer}}{L_{2}} \\ -\frac{v_{peer}}{L_{2}} \end{bmatrix}$$
(20)

Using the mode in Equation (19), a closed-loop discrete state observer is built, shown in Equation (21):

$$\begin{cases} \hat{x}(k+1) = A_d \hat{x}(k) + B_d u(k) + D_d(k) + L[\hat{y}(k) - y(k)] \\ \hat{y}(k) = C_c \hat{x}(k) \end{cases}$$
(21)

where $C_c = \begin{bmatrix} 0 & 0 & 1 \end{bmatrix}^T$, $L = \begin{bmatrix} l_1 & l_2 & l_3 \end{bmatrix}^T$ and the superscript "^" denotes the estimated value.

As shown in Figure 4, *L* denotes the observer feedback gain matrix. It determines the dynamic characteristics of the state observer closed-loop system and is used for faster convergence of observations to actual values.



Figure 4. Discretization control diagram of state observer closed-loop system of grid-connect VSI.

A necessary condition in designing a state observer is that the system must be observable so that the eigenvalues of the observer can be arbitrarily assigned. According to Equation (22) it can be seen that the system has full rank and the system is observable.

$$\operatorname{rank}\begin{bmatrix} C_c \\ C_c A_d \\ C_c A_d^2 \end{bmatrix} = 3 \tag{22}$$

Generally speaking, if the poles of the observer are chosen to be three to five times faster than those of the controller [28], the dynamic characteristics of the observer will not impose any restrictions on the bandwidth determined by the controller. However, the observer's poles should not exceed the Nyquist frequency.

If the characteristic polynomial of the observer dynamics is selected as:

$$\det(zI_3 - A_d + LC_c) = (z - p_1)(z - p_2)(z - p_3)$$
(23)

where p_1 , p_2 and p_3 are the desired poles of observer, then observer gain vector can be calculated.

According to the principle of separation, the design of a state observer and a current controller do not affect each other. This is very advantageous to the overall design of the system so that the sliding mode controller and the state observer can be designed independently, which is more convenient.

3.3. Modified SMC Controller with Active Damping

As explained previously, this section will provide a modified SMC controller with active damping to increase the control bandwidth and improve the robustness against the grid impedance variations based on Sections 3.1 and 3.2. As inspired by a well-known active damping method, the capacitor voltage obtained by the state observer is added in the conventional SMC control loop. The modified control block diagram is shown in Figure 5.



Figure 5. Control block of the modified SMC of grid-connected VSI in $\alpha\beta$ -frame.

The modified overall expression of the SMC switching function can be written in Equation (24):

$$\begin{cases} u_{i_{\alpha}} = L_1 k_p s_{\alpha} + L_1 \varepsilon sat(s_{\alpha}) + L_1 i_{1\alpha}^* + r_1 i_{1\alpha}^* - v_{c\alpha} + k_{damp} C \frac{dv_{c\alpha}}{dt} \\ u_{i_{\beta}} = L_1 k_p s_{\beta} + L_1 \varepsilon sat(s_{\beta}) + L_1 i_{1\beta}^* + r_1 i_{1\beta}^* - v_{c\beta} + k_{damp} C \frac{dv_{c\beta}}{dt} \end{cases}$$

$$(24)$$

Then, the closed-loop transfer function of the modified SMC system is shown in Equation (25):

$$G_{close+ad}(s) = \frac{I_1(s)}{I_1^*(s)} = \frac{c_3 s^3 + c_2 s^2 + c_1 s + c_0}{d_3 s^3 + d_2 s^2 + d_1 s + d_0}$$
(25)

where:

$$c_{3} = CL_{2}L'_{1}$$

$$c_{2} = CL_{2}k_{p} + CL_{2}r'_{1} + CL'_{1}r_{2}$$

$$c_{1} = L'_{1} + Ck_{p}r_{2} + Cr_{2}r'_{1}$$

$$c_{0} = k_{p} + r'_{1}$$

$$d_{3} = CL_{1}L_{2}e^{1.5T_{s}\cdot s}$$

$$d_{2} = CL_{2}k_{p} + CL_{2}k_{damp} + CL_{1}r_{2}e^{1.5T_{s}\cdot s} + CL_{2}r_{1}e^{1.5T_{s}\cdot s}$$

$$d_{1} = L_{2} + L_{1}e^{1.5T_{s}\cdot s} + L_{2}e^{1.5T_{s}\cdot s} + Ck_{p}r_{2} + Cr_{1}r_{2}e^{1.5T_{s}\cdot s}$$

$$d_{0} = k_{n} + r_{2} + r_{1}e^{1.5T_{s}\cdot s} + r_{2}e^{1.5T_{s}\cdot s}$$
(26)

In order to analyze the performance of a modified SMC controller, the frequency responses of the open-loop transfer functions of the traditional SMC method and modified SMC method have been plotted, as shown in Figure 6. It can be seen that the resonance peak is damped when the capacitor voltage loop is added into the control loop.



Figure 6. Bode plots of open-loop control gain with traditional and modified control methods.

To be clear, the eigenvalues of the modified closed-coop system in the z plane are provided in Figure 7. After adding the capacitor voltage loop in the control loop, the LCL poles are attracted inside the unity circle and the critical value of the proportional gain k_p has increased to 12, which means the stability margin is enlarged and the bandwidth can be increased.



Figure 7. System poles of modified SMC when k_p varies from 1 to 12.

3.4. Stability Analysis

To prove the robustness of VSI using the proposed control method and to select the controller parameters, a stability analysis was performed in this part. The system stability is ensured if the poles of the closed-coop system are inside the unity circle in the z plane.

In order to operate the closed-loop system with a reasonable damping effect, all poles should be placed far away from the border of the unit circle in z plane. Considering the dynamic response and the bandwidth of the VSI system for PR harmonics compensation, the proportional gain of the PR controller k_p is selected as 10. Figure 8a shows the root locus diagrams when the damping gain k_{damp} varies from 5 to 13. Hence, k_{damp} should be selected between the critical values to ensure a high damping effect and system robustness.



Figure 8. Root locus diagrams when (a) the damping gain k_{damp} varies; (b) grid impedance L_g varies.

Figure 8b shows the movement of the closed-loop poles with different values of grid impedance L_g . With L_g increasing from 0 to 4.8 mH, the poles always move inside the unit circle, which shows good validity of the proposed control algorithm and proves that the system can be stabilized by using a modified SMC under a weak grid.

4. Experimental Results

In order to validate the effectiveness and performance of the proposed control algorithm, the experimental results are obtained on a 3 kW Danfoss FC302 converter tied to the grid through a LCL filter, as shown in Figure 9. Grid-side current and PCC voltage are sensed for a control strategy. The proposed control scheme was implemented in a digital signal processor dSPACE DS1202 controller with a sampling frequency of 12 kHz. The power grid and the DC voltage have been generated using a Chroma 61830 three-phase grid simulator and a Chroma 62150H-600S DC power supply, respectively. The grid voltage and grid-injected current waveforms were observed by Yokogawa DL 1640 digital oscilloscope. The implemented system is depicted in Figure 9 and parameters are listed in Tables 1 and 2 for testing.

According to the analysis above, the proposed SMC method is simple and effective with less sensors. Three cases are discussed in the following experimental results: (1) the effect of the capacitor voltage loop; (2) behavior with changes in the reference current value and grid inductance variation; and (3) performance under grid voltage distortion.



Figure 9. LCL-filtered three-phase grid-connected VSI experimental prototype.

| Table 2. Control | er parameters for e | experimental results. |
|------------------|---------------------|-----------------------|
|------------------|---------------------|-----------------------|

| Symbol | Value |
|---|-------|
| Coefficient of control k_p | 10 |
| Coefficient of control k_r | 800 |
| Coefficient of control <i>k</i> _{damp} | 8 |

4.1. Test of Active Damping Effect

As discussed in previous sections, the modified SMC control with capacitor voltage loop feedback has a damping effect for LCL resonance. In this way, the system control bandwidth can be increased and the grid current can meet the harmonics compensation requirement with a PR controller. Figure 10a shows the experimental results of PCC voltages and grid currents for Phase a and b with $k_{damp} = 0$, $k_p = 10$. It can be seen from the figure that the system is unstable with oscillations in the currents. Then, the experimental results in Figure 10b have shown that an active damping loop takes effect and the system is stable when the k_{damp} is changed to eight. After enabling the damping, the system becomes stable and the THD of the grid current is 2.5%. Figure 11 shows the PCC voltages and converter-side currents with the proposed SMC method. The state observer works well with the SMC control and the results verify the correctness of the control parameters selection.



Figure 10. Measured waveforms of PCC voltages and grid currents (**a**) without active damping; (**b**) with active damping.



Figure 11. Measured waveforms of PCC voltages and converter-side currents.

4.2. Performance of the Grid-Connected VSI against Sudden Changes in the Reference Current and Grid Inductance Variation

Figure 12a presents the grid current and PCC voltage waveforms of the grid-connected VSI when a sudden reference current change occurs, from full-load to half-load with the proposed SMC method. It presents a variation in the reference current i_1^* from 12.8 A to 6.4 A. This result clearly demonstrates the effect of using a PR controller in achieving zero steady-state error and fast dynamic response in the grid current.



Figure 12. Measured waveforms of PCC voltages and grid currents with proposed SMC when (a) the reference current changes; (b) the grid inductance varies.

The performance of the grid-connected VSI under grid inductance variation with proposed SMC method is also presented in Figure 12b. In the test, the grid inductance is increased from 0–4.8 mH. As it can be seen, with the modified control method the system keeps stable even though it is in the weak grid. It can be concluded that all the roots are attracted inside the unit circle in accordance with the analysis in Section 3.4, which proves the high robustness of the control algorithm against grid inductance variations without a tuning algorithm.

4.3. Test of the Grid-Connected VSI under Grid Distortion

Figure 13 shows the experimental steady-state results of PCC voltage and grid current of grid-connected VSI by the proposed control strategy under grid distortion. Even though the grid voltages are injected 3% for the third, fifth and seventh harmonic components, the grid current

waveforms are still sinusoidal, realizing the good harmonic compensation and zero steady-state error by a PR controller.



Figure 13. Measured waveforms of PCC voltages and grid currents with proposed SMC under distorted grid voltage.

5. Conclusions

In this paper, an observer-based sliding mode control for LCL-filtered grid-connected inverters with improved stability was presented. Traditionally, the implementation of a sliding mode control is always accompanied by multiple state variables feedback, which requires more sensors. The proposed control strategy can reduce two sensors by using a state observer. The state observer can be combined well with the discrete sliding mode control in digital implementation. In addition, the use of an estimated capacitor voltage can provide an active damping capability to the system by eliminating the LCL resonance. Furthermore, a detailed parameter design of the proposed SMC method was provided and analyzed when considering the digital discretization. The experimental results obtained from a 3 kW system shows that the proposed control strategy achieved a good performance, especially in its robustness against a wide range of grid inductance variations, and the state observer did not influence the control effect of the system.

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