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Charge Equalization Controller Algorithm for Series-Connected Lithium-Ion Battery Storage Systems: Modeling and Applications

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Abstract: This study aims to develop an accurate model of a charge equalization controller (CEC) that manages individual cell monitoring and equalizing by charging and discharging series-connected lithium-ion (Li-ion) battery cells. In this concept, an intelligent control algorithm is developed to activate bidirectional cell switches and control direct current (DC)–DC converter switches along with pulse width modulation (PWM) generation. Individual models of an electric vehicle (EV)-sustainable Li-ion battery, optimal power rating, a bidirectional flyback DC–DC converter, and charging and discharging controllers are integrated to develop a small-scale CEC model that can be implemented for 10 series-connected Li-ion battery cells. Results show that the charge equalization controller operates at 91% efficiency and performs well in equalizing both overdischarged and overcharged cells on time. Moreover, the outputs of the CEC model show that the desired balancing level occurs at 2% of state of charge difference and that all cells are operated within a normal range. The configuration, execution, control, power loss, cost, size, and efficiency of the developed CEC model are compared with those of existing controllers. The proposed model is proven suitable for high-tech storage systems toward the advancement of sustainable EV technologies and renewable source of applications.

Keywords: charge equalization controller; control algorithm; modeling; state of charge; lithium-ion battery; sustainable energies

1. Introduction

Sustainable energies and electric vehicles (EVs) have attracted increasing attention as an alternative to conventional energy systems because of their contributions to green global environment and fossil fuel demand reduction. Developing competent EVs with efficient energy management and storage systems is a great concern to automobile developers and researchers [1–3]. Among secondary batteries, lithium-ion (Li-ion) batteries are currently the best candidate in hybrid electric vehicle (HEV), plug-in hybrid electric vehicle (PHEV), and battery-powered electric vehicle (BEV) storage applications because of their high storage capability, high energy and power densities, low hysteresis, no memory effects, long life cycle duration, and high terminal voltage [1,4].

Li-ion batteries are applied for large-scale energy storage systems in EVs with a number of series-connected battery cells in a pack. Li-ion batteries provide high voltage and power in high

specific energy and power with a low self-discharge rate to EV motors [5,6]. However, these batteries are affected by the imbalance voltage properties of battery cells because of changes in their physical characteristics after a few charge–discharge cycles. Imbalance voltage profiles may reduce the overall performance and durability of energy storage systems [7,8]. Overcharging might cause cell explosion, whereas overdischarging might damage the chemical properties and thus shorten the life of battery cells [9]. A battery management system is essential to protect Li-ion battery cells from overcharging and overdischarging [10].

A battery management system with individual cell monitoring and charge equalization could save battery cells from anomalies caused by overcharging and overdischarging and enhance the overall performance of an energy storage system to effectively power EVs [11,12]. Modeling of a charge equalization controller is an imperative concern that has the key rule to monitor cell conditions and equalize cell charges within a normal operating range.

Various studies and developments on passive and active charge equalization techniques have been carried out to equalize the charges of series-connected battery cells [12,13]. Resistor shunting or analog shunting as a passive charge equalization controller features a simple design and implementation, but suffers from heat and energy dissipation problems [12–14]. Active equalization controllers could be based on capacitors, inductors, transformers, and converters to transfer excess energy from an overcharged cell to other-cell/module/pack or to carry the equalization current to an overdischarged cell from other-cell/module/pack [12,13,15]. Capacitor-based charge equalization controllers are applied to shuttle excess energy via capacitors, which are easy to design; however, capacitors possess large current ripples and need long equalization times [16,17]. Equalization controllers based on inductors and transformers require short equalization times and feature a high speed and current; however, these controllers suffer from magnetizing loss, need a capacitor filter, and are expensive [18,19]. Flyback [20], resonant [21,22] and buck-boost [23,24] converter-based charge equalization controllers are efficient in EV applications with various topologies. These controllers demonstrate bidirectional balancing capabilities, fast equalization speed, high efficiency, and low stress, but they require complex control and design, and are costly [12–25]. All existing controllers sufficiently meet the present demand for battery cell equalization in EVs. However, they show disadvantages in terms of equalization time, power loss, heat management, cost, design, and voltage/current stress [12,13,25].

For protecting and fault detecting the battery with efficient charge equalization, the state of charge (SOC) and state of health (SOH) estimation is important in energy storage applications [26]. SOC and SOH estimation have proved challenging for developing and improving battery technologies because of changes in batteries' electrochemical profile, variation of ambient and load conditions, etc. [27]. A number of studies have been accomplished to predict the SOC and SOH of battery by a variety of methods such as the model predictive control method [28], open circuit method [29], dynamic method [30], adaptive sliding mode observer method [31], spherical cubature particle filter method [32], particle swarm optimization method [33], Kalman filter method [34], data handling group method [35], relevance vectors and capacity degradation model [36], state space model [37] and others [38–40]. These different estimation methods have been applied as the service requirement and compatibility with the system, especially battery charge equalization, however, SOC and SOH estimation still have big challenges due to the internal and external factors of batteries.

With the progress of research and development aiming to minimize the deficiencies of existing charge equalization controllers, a model of charge equalization controller is proposed to diminish the problems related to equalization time, design and control complexity, and voltage/current stress. The controller is implemented to monitor and equalize the charge levels of series-connected Li-ion battery cells with a bidirectional flyback converter by using an equalization control algorithm. This model features a simple operation design, good converter efficiency, high equalization speed, tolerable voltage/current stress to switches, and good control of power converter with PWM signals

upholding a steady equalization current. With better performance, the CEC model might have prospects to be implemented in real time EV energy storage systems.

2. Overview of Charge Equalization Controller

The charge equalization control model comprises five parts, namely, series-connected Li-ion battery cells, a bidirectional flyback converter, an equalization control function, bidirectional cell switches, and a converter access switch as shown in Figure 1. The series-connected battery cells are arranged with bidirectional switches so that any cell can link up the converter via the converter access switch. All positive and negative polarities of battery terminals are joined to a common node alone through the bidirectional cell switches. The equalization control function regularly communicates to monitor the charge conditions of the cells. In particular, if a cell is detected as overcharged, overdischarged, or unequalized, the equalization control function selects the corresponding bidirectional cell switch, turns ON the converter access switch, and activates the operation of the flyback converter by sending control signals through the equalization control algorithm.

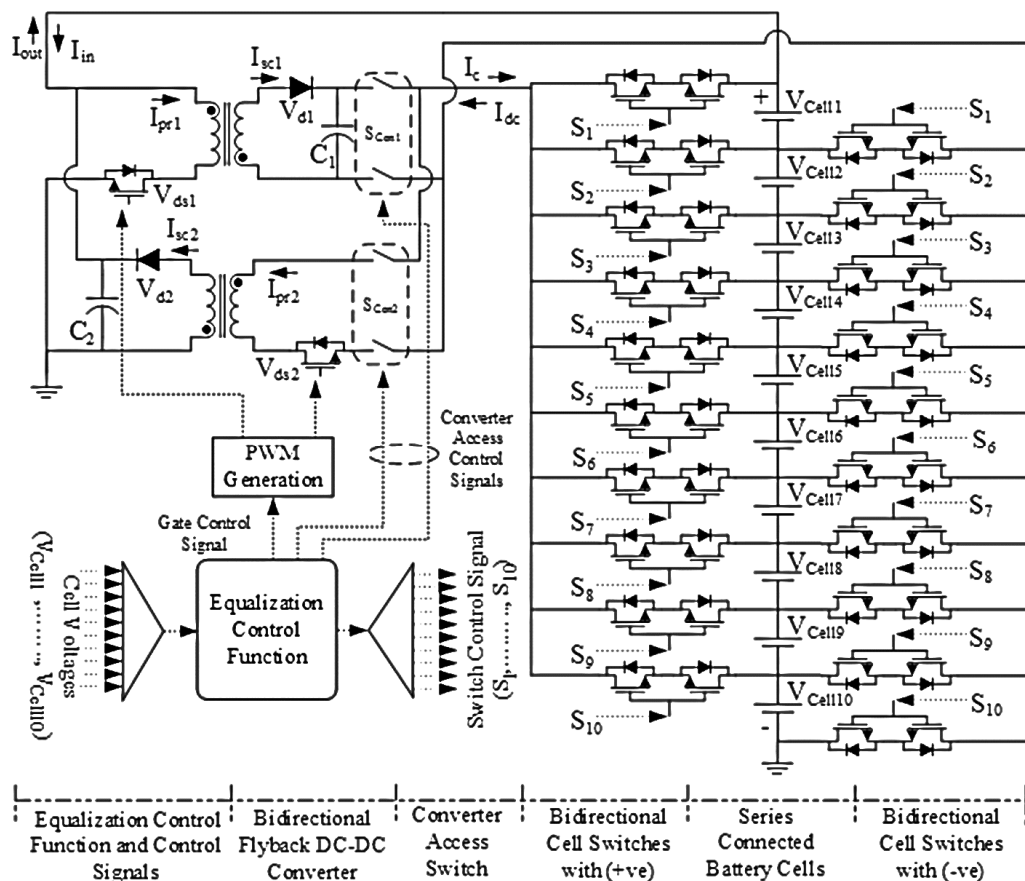


Figure 1. Circuit model of a charge equalization controller.

The equalization control function allows the unequalized cell, that is, overcharged or overdischarged cell, to be discharged or charged by stimulating the bidirectional flyback DC-DC converter with PWM signals, whether it might be the reverse flyback converter for discharging or the forward flyback converter for charging. The overall process of the equalization controller is straightforward and efficient to acquire the perfect equalization for individual battery cells. The features of the proposed charge equalization controller model are as follows:

- (1) The overall process of the equalization controller is straightforward and efficient to be executed.
- (2) The equalization controller is active and thus is free of heat and energy dissipation problems.

- (3) The equalization control algorithm can be easily customized with the demand for any modification.
- (4) The controller can be applied to any size of battery storage systems with the power and voltage demand for different applications.

3. Modeling of the Charge Equalization Controller

The equalization of series-connected Li-ion battery cells could be ascertained by modeling the charge equalization controller through individual models of an EV Li-ion battery, optimal power rating, power converter, Li-ion battery charge and discharge, and equalization circuit. This section aggregates these models to obtain an efficient equalization controller model for the charge equalization of series-connected Li-ion battery cells in EVs.

3.1. Modeling of EV Lithium-Ion Battery

The essential task to develop a model of charge equalization controllers is to model the EV Li-ion battery in order to model the optimal power rating, implement the equalization operation, and enhance the performance of equalization. A Li-ion battery of nominal 15.5 Ah and 3.7 V is chosen in EV applications because of its high output rating [6–8,12]. The generic model is applied for the Li-ion battery because this model is flexible to configuration at any values for Li-ion, lead-acid, Ni-Cd, and Ni-MH batteries [28]. The generic model is similar to the Shepherd model, except it is free of algebraic loop problems. This model is applicable for both charging Equation (1) and discharging Equation (2) batteries to implement the equalization operation and to enhance its performance. The battery model is considered to be operated at normal operating temperature that required by the EV system although the lithium-ion battery has the negative temperature effect on the internal resistance. When battery is fully charged the voltage increases rapidly. That refers to the polarization resistance in battery model as it increases abruptly until the battery is fully charged due to end-of-charge characteristics of the lithium-ion battery. The battery model uses the SOC as state variable to obtain the accurate manufacturing characteristics. The internal impedance might be determined from the second pulse transient voltage response during the rest period and related with the SOC using empirical equation by polynomial curve fitting approximation [29].

Charging model of Li-ion battery:

$$V_{ch} = E_0 - K \frac{Q}{Q' + 0.1Q} \cdot i^* - K \frac{Q}{Q' - Q} Q' + Ae^{-BQ'} - R \cdot i \quad (1)$$

Discharging model of Li-ion battery:

$$V_{disch} = E_0 - K \frac{Q}{Q - Q'} \cdot i^* - K \frac{Q}{Q - Q'} Q' + Ae^{-BQ'} - R \cdot i \quad (2)$$

where V_{ch} is the battery voltage at charge, V_{disch} is the battery voltage at discharge, E_0 is the constant voltage (V), R is the internal resistance (Ω), K is the polarization constant (Ah^{-1}) or polarization resistance (Ω), i is the battery current (A), Q' is the extracted capacity (Ah), that is, $\int_0^t i(t) dt$, Q is the maximum battery capacity (Ah), A is the exponential voltage (V), B is the exponential capacity (Ah^{-1}), and i^* is the low frequency current (A).

The voltage profiles of the Li-ion battery model at different charge and discharge rates are shown in Figure 2a,b, respectively. These profiles are measured with a current pulse of 1 C, C/2, and C/3 charge and discharge rates for 300 s at 300 s intervals. The waveforms in Figure 2a,b show the uniform characteristics in all charge and discharge profiles.

The open circuit voltage (OCV) is counted using the nonlinear equation on the basis of the actual SOC of the battery as Equations (1) and (2). The model evaluation of the Li-ion battery in terms of the OCV vs. SOC relationship representing the data characteristics is shown in Figure 3 [12,23]. The OCV curve is obtained at SOC variations with the consideration of constant internal resistance

and temperature with nonlinear model equation and actual battery charge. The relationship model of the Li-ion battery is applied to model the optimal power rating and to fix the equalization conditions on the basis of the cell voltage and SOC difference. Equation (3) presents the approximate relationship between the OCV (V_{OC}) and SOC of the Li-ion battery found by curve fitting the experimental values with a norm of residuals of 0.016062 from the Figure 3. The SOC–OCV relationship is used to estimate SOC of the Li-ion battery [7,9,28–40] for implementing the equalization control algorithm of the equalization controller model. During the process of charge equalization the controller records the terminal voltage value of each cell and converts to SOC value of the respective cell by using the relationship Equation (3), considering negligible temperature effect on the internal resistance of battery model. The controller detects the unprotected, overcharged, or overdischarged cell based on the SOC difference value between the average SOC and the SOC value of the specific detect cell:

$$V_{OC} = 21.049 \times SOC^5 - 57.837 \times SOC^4 + 62.228 \times SOC^3 - 32.997 \times SOC^2 + 8.9149 \times SOC + 2.824 \quad (3)$$

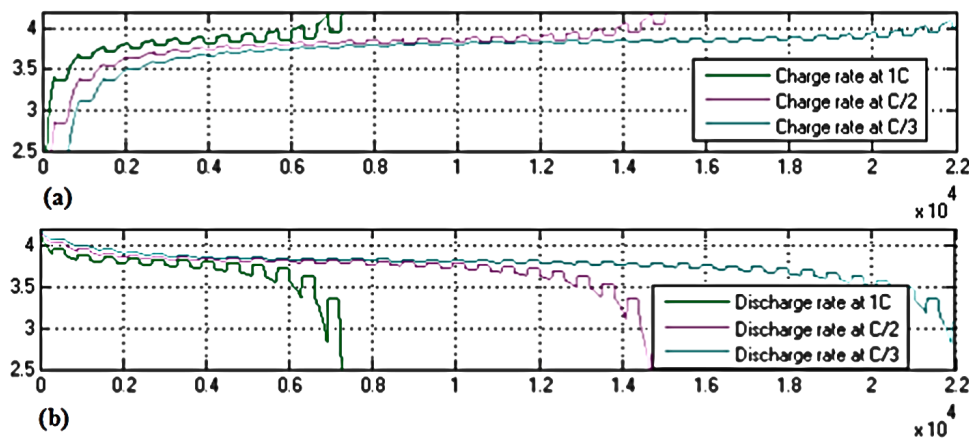


Figure 2. Voltage profile of the lithium-ion battery model at different (a) charge rates and (b) discharge rates.

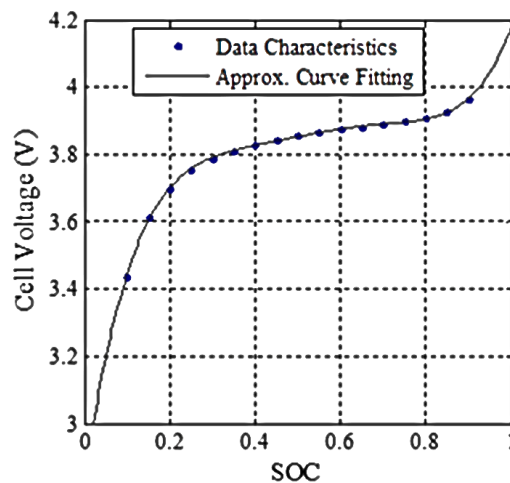


Figure 3. OCV and SOC relationship model of the Li-ion battery.

3.2. Modeling of Optimal Power Rating

The equalization controller model is applicable for n battery cells of a pack connected in series. As discussed in the preceding section, the fifth cell is considered as overdischarged. Thus, the battery cell needs to be charged. With this assumption, the optimal power rating could be obtained by

satisfying Equations (4) and (5). Equation (4) means that the amount of charge Q_5 stored in Cell-5 over the equalization period T_{eq} must be equal to the mean charge transferred from all other cells at the same duration. Equation (5) means that the average output power $P_{out_average}$ that is extracted from the cells excluding the overdischarged cell is the same as the converter efficiency η times the average input power $P_{in_average}$ that is transferred in the overdischarged cell:

$$Q_5(T_{eq}) = \frac{1}{n-1} \left(\sum_{i=1}^4 Q_i(T_{eq}) + \sum_{k=6}^n Q_i(T_{eq}) \right) \quad (4)$$

$$P_{out_average} = \eta \times P_{in_average} \quad (5)$$

The amount of charge in Cell-5 $Q_5(t)$ at time t , the average output power $P_{out_average}$, and the average input power $P_{in_average}$ of the equalization controller are presented in Equations (6)–(8), respectively, in terms of the equalization current I_{eq} , the equalizer input current I_{in} , the equalizer output current I_{out} , and the capacitance C of the 15.5 Ah Li-ion battery:

$$Q_5(t) = Q_5(0) + Q_5(T_{eq}) = Q_5(0) + I_{eq} \cdot T_{eq} = Q_5(0) + (I_{out} - I_{in}) \cdot T_{eq} \quad (6)$$

$$\begin{aligned} P_{out_average} &= \frac{1}{T_{eq}} \int_0^{T_{eq}} P_{out}(t) dt = \frac{1}{T_{eq}} \int_0^{T_{eq}} V_5(t) \cdot I_{out} dt = \frac{1}{T_{eq} \cdot C} \int_0^{T_{eq}} Q_5(t) \cdot I_{out} dt \\ &= \frac{1}{T_{eq} \cdot C} \int_0^{T_{eq}} (Q_5(0) + (I_{out} - I_{in}) \cdot t) \cdot I_{out} dt \\ &= \left(V_5(0) + \frac{1}{2C} (I_{out} - I_{in}) \cdot T_{eq} \right) \cdot I_{out} \end{aligned} \quad (7)$$

$$\begin{aligned} P_{in_average} &= \frac{1}{T_{eq}} \int_0^{T_{eq}} P_{in}(t) dt = \frac{1}{T_{eq}} \int_0^{T_{eq}} \left(V_5(t) + \sum_{i=1}^4 V_i(t) + \sum_{i=6}^n V_i(t) \right) \cdot I_{in} dt \\ &= \frac{1}{T_{eq} \cdot C} \int_0^{T_{eq}} \left(Q_5(t) + \sum_{i=1}^4 Q_i(t) + \sum_{i=6}^n Q_i(t) \right) \cdot I_{in} dt \\ &= \left(\sum_{i=1}^n V_i(0) + \frac{1}{2C} (I_{out} - n \cdot I_{in}) \cdot T_{eq} \right) \cdot I_{in} \end{aligned} \quad (8)$$

The relationships between the equalization period T_{eq} and the equalization current I_{eq} as well as between the equalization period T_{eq} and the equalizer input current I_{in} are determined using Equations (9) and (10), respectively, by solving Equations (4) and (5) with the values from Equations (6)–(8):

$$\begin{aligned} T_{eq} &= \frac{C}{I_{eq}} \left(\left(\frac{1}{n-1} \left(\sum_{i=1}^4 V_i(0) + \sum_{i=6}^n V_i(0) \right) - V_5(0) \right) \right. \\ &\quad \left. - \left(\frac{1}{n} \sum_{i=1}^n V_i(0) + \sqrt{\left(\frac{1}{n} \sum_{i=1}^n V_i(0) \right)^2 - \frac{1}{\eta \cdot n} \left(\left(\frac{1}{n-1} \left(\sum_{i=1}^4 V_i(0) + \sum_{i=6}^n V_i(0) \right) \right)^2 - (V_5(0))^2 \right)} \right) \right) \end{aligned} \quad (9)$$

$$T_{eq} = \frac{C}{I_{in}} \left(\frac{1}{n} \sum_{i=1}^n V_i(0) + \sqrt{\left(\frac{1}{n} \sum_{i=1}^n V_i(0) \right)^2 - \frac{1}{\eta \cdot n} \left(\left(\frac{1}{n-1} \left(\sum_{i=1}^4 V_i(0) + \sum_{i=6}^n V_i(0) \right) \right)^2 - (V_5(0))^2 \right)} \right) \quad (10)$$

These two relationships are utilized to estimate the equalization time by fixing the equalization current so that the equalization converter produces minimal stress to the switches and rectifier diodes and the charge equalization controller model performs in accord with the design considerations. To simplify the modeling of the charge equalization controller for a large number of series-connected battery cells in a pack, the equalization current I_{eq} could be obtained using Equation (11), which is

approximately equal to the equalizer output current I_{out} and the equalization period T_{eq} with respect to the SOC in Equation (12):

$$I_{eq} = I_{out} - I_{in} = I_{out} \left(1 - \frac{I_{in}}{I_{out}} \right) \cong I_{out} \left(1 - \frac{V_5}{\eta \sum_{i=1}^n V_i} \right) \cong I_{out} \quad (11)$$

$$T_{eq} = \frac{Q_5(T_{eq})}{I_{eq}} = \frac{Q_{Total}}{I_{eq}} \cdot SOC_5(T_{eq}) \cong \frac{Q_{Total}}{I_{out}} \cdot SOC_5(T_{eq}) \quad (12)$$

where Q_{Total} is the total capacity of a single battery cell and $SOC(T_{eq})$ is the SOC difference level within T_{eq} noted by ΔSOC .

3.3. Modeling of Bidirectional Flyback DC–DC Power Converter

The charge equalization controller needs the consistent design of the power converter. Meanwhile, it primarily deals with the transfer of equalization current to equalize the imbalanced battery cell. A flyback DC–DC power converter is utilized in this proposed system, as shown in Figure 4, which transfers power by storing power in the electromagnetic field of a transformer magnetizing coil in the primary circuit during the duty ON stage of the switch ($0 < t \leq DT$); then, the stored power is delivered to the secondary circuit during the duty OFF stage of the switch ($DT < t \leq T$). The operation of the flyback DC–DC converter is considered with discrete current mode so that it can perform reliably without the reverse recovery blocking problem in the rectifier diode [20,41,42].

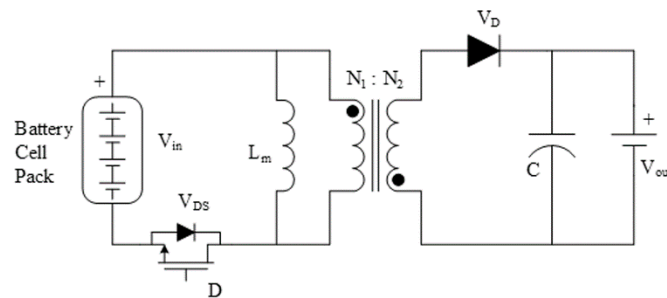


Figure 4. Typical circuit diagram of the flyback DC–DC converter.

The modeling of the flyback DC–DC power converter is designed to configure its electrical components with the best parameter values which has the low and tolerable voltage and current stresses on the power metal-oxide-semiconductor field-effect transistor (MOSFET) switch and rectifier diode and with the desired power, voltage, and current levels. For this, the converter components need to be selected with the maximum stress value so that the switch can operate safely without any peak voltage and current effect [43–45]. The maximum duty of switching PWM signal D_{max} for the flyback DC–DC converter is considered to operate the switch in the safe region of voltage stress calculated using Equation (13) [46]:

$$D_{max} = \frac{V_{ds_nom} - V_{in_min}}{V_{ds_min} + V_{ds_nom} - V_{in_max}} \quad (13)$$

where V_{in_min} is the minimum voltage for the battery cell pack, V_{in_max} is the maximum voltage for the battery pack, V_{ds_min} is the minimum voltage stress of the switch, and V_{ds_nom} is the nominal voltage stress of the switch ($\geq 30\%$ margin of maximum drain–source voltage).

The mutual inductance of transformer L_m can be counted using Equation (14) [41]:

$$L_m = \frac{(\eta \cdot V_{in_min} \cdot D_{max})^2}{2 \cdot P_{in} \cdot f} \quad (14)$$

where P_{in} is the input power and f is the switching frequency.

The turns ratio of transformer n and the filter capacitor C of the DC–DC converter can be calculated using Equations (15) and (16), respectively [41]:

$$n = \frac{N_1}{N_2} = \frac{\eta \cdot V_{in_min} \cdot D_{max}}{(V_0 - V_f) \cdot (1 - D_{max})} \quad (15)$$

$$C = \frac{\eta \cdot P_{in} \cdot D_{max}}{(V_0 - V_f)^2 \cdot f} \quad (16)$$

where N_1 is the number of primary turns, N_2 is the number of secondary turns, V_0 is the output voltage, V_f is the forward voltage of the rectifier diode, and η is the converter efficiency, which is considered to regulate the required converter output power by setting the mutual inductance of the flyback transformer, transformation ratio, voltage transmission factor, and filter capacitor of the power converter.

The maximum voltage stresses on the MOSFET switch and diode of the flyback DC–DC converter are given in Equations (17) and (18) [41]:

$$V_{S_DS_max} = V_{in_max} + n \cdot V_{out} \quad (17)$$

$$V_{S_D_max} = \frac{V_{in_max}}{n} + V_{out} \quad (18)$$

where $V_{S_DS_max}$ is the maximum voltage stress on the MOSFET switch, V_{in_max} is the maximum input in the primary side, V_{out} is the output voltage, and $V_{S_D_max}$ is the maximum voltage stress on the diode.

The total power loss of the converter is calculated using Equation (19) [41]:

$$P_{CL_Total} = P_{L_DS} + P_{L_SW} + P_{L_Pri} + P_{L_Sec} + P_{L_D} + P_{L_Lm} \quad (19)$$

where P_{CL_Total} is the total converter power loss, P_{L_DS} is the MOSFET conduction loss, P_{L_SW} is the switching loss, P_{L_Pri} is the primary winding loss, P_{L_Sec} is the secondary winding loss, P_{L_D} is the diode conduction loss, and P_{L_Lm} is the mutual inductor resistance loss.

The working efficiency of the converter is calculated using Equation (20):

$$\eta = \frac{P_{out}}{P_{in}} = \frac{P_{in} - P_{CL_Total}}{P_{in}} \quad (20)$$

The efficiency of the power converter is initially considered as 85% to obtain the converter parameters so that the total converter power loss could be calculated to find the actual converter efficiency.

3.4. Modeling of Charging and Discharging of Li-Ion Battery

The charging and discharging of Li-ion batteries are important issues in modeling the charge equalization controller to sustain the apt characteristics of the battery. The methods introduced for the charging and discharging of Li-ion batteries are constant current–constant voltage (CC–CV) control [42,46] and discontinuous current mode (DCM) control [41], respectively. These control processes are established for Li-ion batteries with flyback DC–DC converters [41,42].

3.4.1. Constant Current–Constant Voltage Control for Charging

The CC–CV control for Li-ion battery charging is presented in Figure 5. At the beginning of charging, the battery is precharged by the trickle current charge method until the battery voltage exceeds the threshold level because the impedance of the battery with respect to SOC and temperature is affected [26]. Then, the cell is charged as the cell voltage increases exponentially with constant

current by duty adjustment as long as the voltage value reaches the operating mean level. Sequentially, the charging continues with a constant voltage by PI control until the current reaches approximately 0.1 C [26,42].

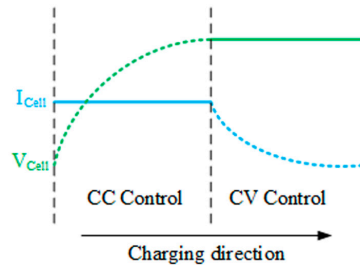


Figure 5. CC–CV control of Li-ion battery charging.

3.4.2. Discontinuous Current Mode Control for Discharging

The DCM control is the basic flyback DC–DC converter operation that operates at three states, namely, ON, OFF, and Reset, in a single switching period Figure 6 [41]. At the ON state of the switch, the primary transformer current and magnetizing inductor current grow with a slope defined by Equation (21), and the magnetizing inductor voltage is the same as the input voltage. The current of the secondary side of the transformer is zero, and the voltage across the diode is defined by Equation (18) [26,41].

$$\frac{di}{dt} = \frac{V_{in}}{L_m} \quad (21)$$

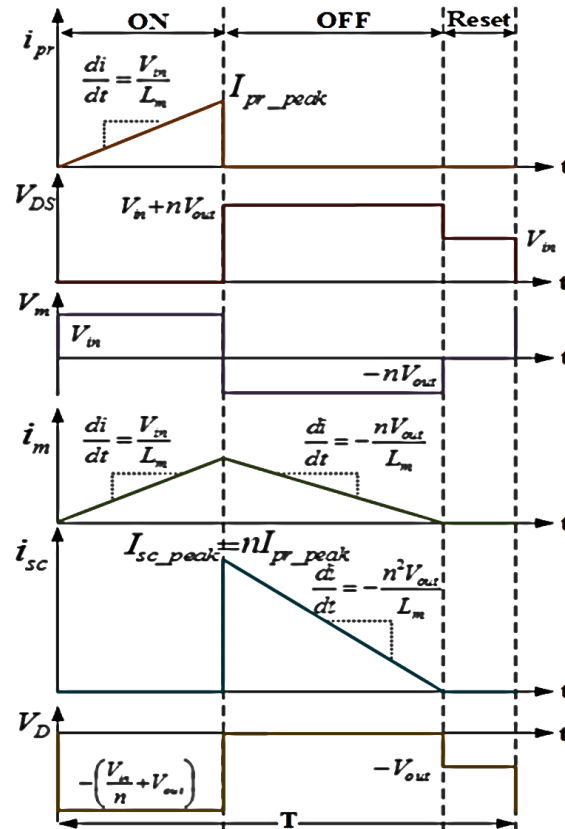


Figure 6. Key waveforms of DCM control of Li-ion battery discharging.

At the OFF state of the switch, the flyback converter transfers the energy to the secondary side for charging the battery as the diode conducts. The secondary current I_{sc_peak} shifts to a level n times that of the primary peak current I_{pr_peak} and then decreases with a slope mentioned in Equation (22). The magnetizing inductor current falls with the slope defined by Equation (23) as the voltage of the magnetizing inductor drops to the output voltage V_{out} multiplied by turns ratio n . As the primary current is zero, the switch voltage stresses are high as presented in Equation (17) [26,41].

$$\frac{di}{dt} = -\frac{n^2 V_{out}}{L_m} \quad (22)$$

$$\frac{di}{dt} = -\frac{n V_{out}}{L_m} \quad (23)$$

At the Reset state, the switch and the diode become unconduted and the magnetizing inductor neither stores nor releases energy. The voltage values across the switch and the diode are as the same as their side values [26,41].

3.5. Modeling of Charge Equalization Controller Circuit

The overview of the charge equalization controller circuit, as shown in Figure 1, is elaborated in Section 2. The controller circuit is modeled through simulation to test and analyze the result of equalization with a set of 10 series-connected Li-ion battery cells. The models of the battery cells are configured with nominal 15.5 Ah and 3.7 V as described in Section 3. To model the small-scale equalization controller, the flyback DC–DC power converter is designed with the values of converter parameters using Equations (13)–(18). To execute the bidirectional operation of the converter and the controller, two flyback DC–DC power converters are utilized as the forward flyback DC–DC converter for charging overdischarged cells and as the reverse flyback DC–DC converter for discharging overcharged cells. The converter model parameters are presented in Table 1, considering a converter input power of 10 W and a switching frequency of 100 kHz.

Table 1. Flyback DC–DC converter model parameters.

Type	Parameter	Value	Unit
Converter Model Parameters	Input power, P_{in}	10	Watts
	Frequency, f	100×10^3	Hz
	Sampling time, T_s	1×10^{-6}	Sec
Forward flyback DC–DC converter	Turns ratio, $N_1:N_2$	80:35	-
	Duty cycle, D_{max}	0.5	-
	Mutual inductance, L_m	0.8×10^{-4}	H
	Filter capacitance, C_1	3×10^{-6}	F
	Maximum switch voltage stress, $V_{S_DS_max}$	63.76	Volts
	Maximum diode voltage stress, $V_{S_D_max}$	27.89	Volts
Reverse flyback DC–DC converter	Turns ratio, $N_1:N_2$	35:80	-
	Duty cycle, D_{max}	0.85	-
	Mutual inductance, L_m	0.42×10^{-5}	H
	Filter capacitance, C_2	0.66×10^{-6}	F
	Maximum switch voltage stress, $V_{S_DS_max}$	22.70	Volts
	Maximum diode voltage stress, $V_{S_D_max}$	51.88	Volts

The bidirectional cell switches of the MOSFET switch and the converter access switches of relay are used to construct the electric pathway between the converter and the battery cells. The control function is embedded with the algorithm to detect the unprotected and unequalized cell with the conditions of overcharge, overdischarge, and minimum ΔSOC , and to activate all the consecutive processes. The control processes are executed by the controller such that the equalization is performed accordingly by transferring the equalization current from a cell pack to an overdischarged cell or vice

versa. The equalization in the proposed charge equalization controller is maintained by distributing the energy from the battery cells among the cells or cell on the basis of the charge or discharge operation without an extra power source or sink, respectively.

4. Charge Equalization Controller Algorithm

The charge equalization controller is operated to equalize the series-connected battery cells by exchanging energy through the equalization current from an unequalized cell to a cell pack or vice versa employing the equalization controller algorithm as shown in Figure 7. The equalization of series-connected battery cells is a continuous process. After the initialization of the system, the equalization control function acts for cell monitoring to record the cell voltage levels and determine whether the cell conditions are operating within the normal range (i.e., 3.72–3.88 V). The control function calculates the average of cell voltages and tests the charge level of battery cells with the maximum difference of the state of charge (ΔSOC) among all of the cells in terms of the OCV vs. SOC relationship. If a cell is detected as unprotected (i.e., it is out of the normal operating range) or unequalized (i.e., it has more or less 2% ΔSOC than the average value), the equalization control function activates the bidirectional cell switch, turns ON the converter access switch depending on whether the detected cell is overcharged or overdischarged, and finally starts the flyback DC–DC converter operation with PWM signals to the MOSFET switch to allow the steady equalization current to flow until the cell becomes equalized. The bidirectional flyback DC–DC converter is composed of two unidirectional flyback DC–DC converters, namely, forward and reverse flyback converters, for charging and discharging battery cells, respectively.

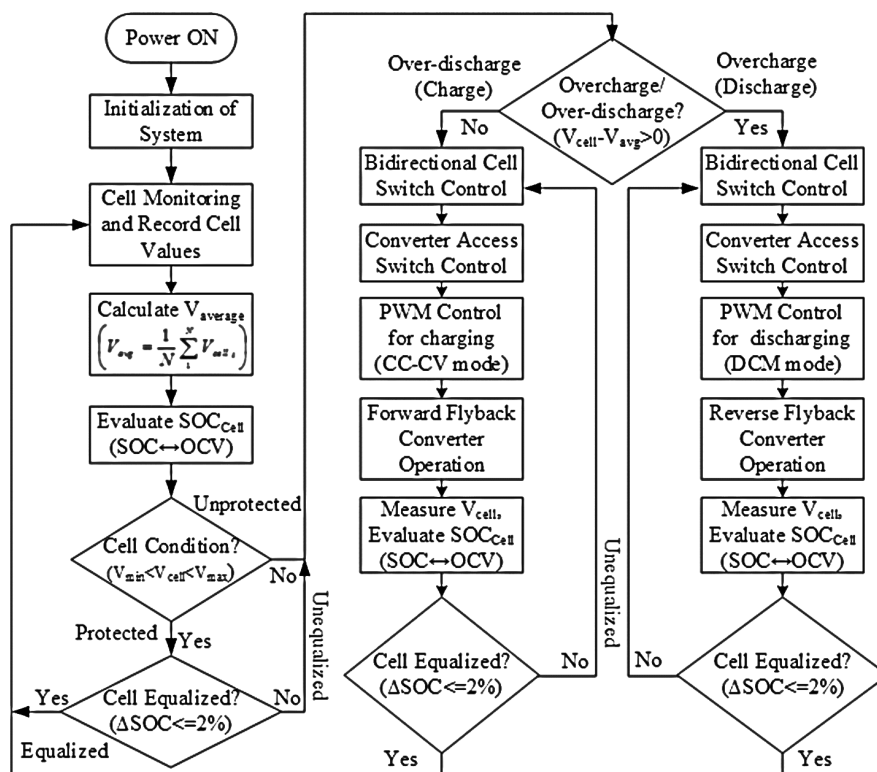


Figure 7. Control algorithm of the flow process of the charge equalization controller.

The charge equalization controller follows the control algorithm as stated in Figure 7 and executes the operation flow route. The control function communicates all the bidirectional cell switches, converter access switches, and converter MOSFET gates to stimulate the whole controller for the equalization process as the switches, diodes, and converters act as ideal. For clear elaboration of

the equalization controller circuit model, the operation processes are illustrated in three steps by considering the battery Cell-5 as an overdischarged cell from the series-connected cell pack:

Step 1. When the equalization controller identifies Cell-5 as overdischarged, it sends “1” logic to bidirectional cell switch, (S_5) corresponding to Cell-5, and “0” logic to that associated with all other cells. A pathway of the current flow for Cell-5 is shown in Figure 8a.

Step 2. As shown in Figure 8b, the equalization control function sends the converter access switch signal to the converter access switch to build an electric path for the access of the forward flyback converter because the detected cell is overdischarged.

Step 3. In this step, the converter MOSFET gate is fired with PWM signals to activate the operation of the forward flyback converter by sending gate control signals. Finally, an equalization current path is generated through the converter to transfer the required energy to the detected Cell-5 from the cell pack as depicted in Figure 8c. Hence, the equalization is performed along with the consequences of the three steps by means of the equalization control algorithm.

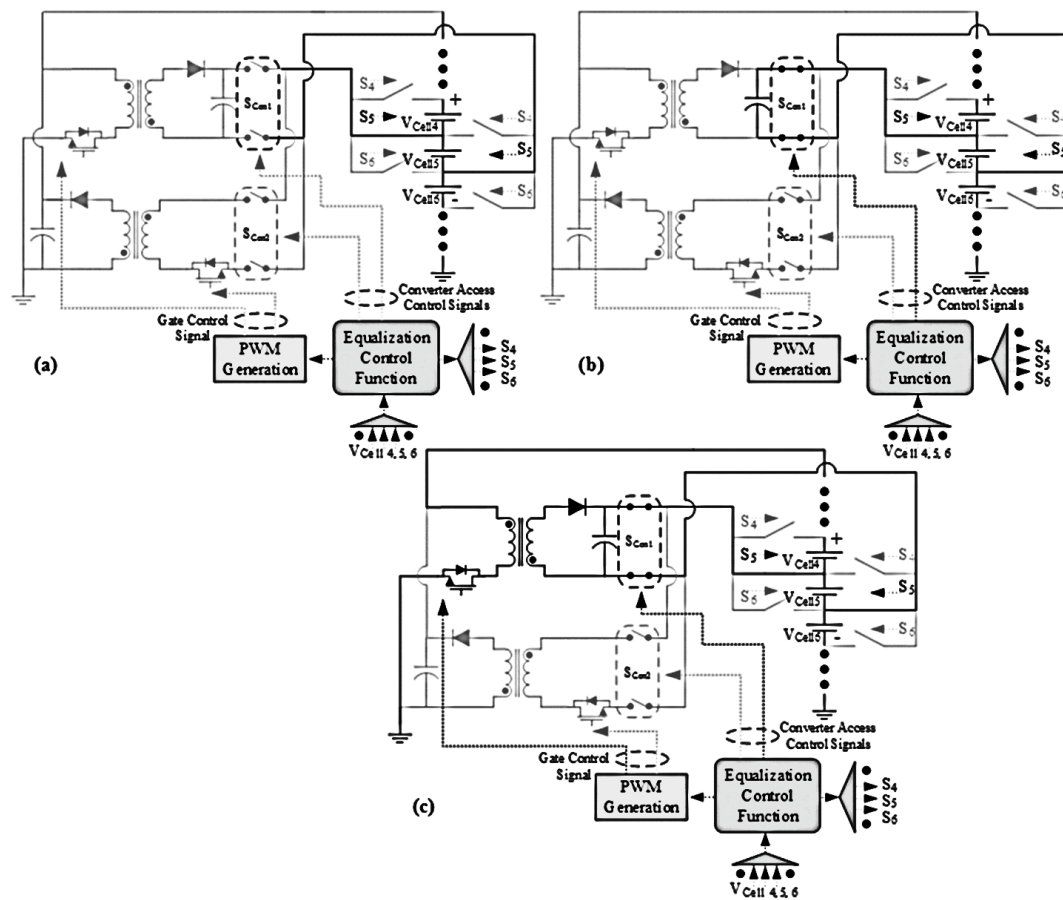


Figure 8. Operational steps of the charge equalization controller model. (a) Step 1; (b) Step 2; (c) Step 3.

The charge equalization controller algorithm allows the transfer of excessive energy from an overcharged cell to a cell pack or the transfer of required energy from a cell pack to an overdischarged cell through the converter. The proposed algorithm operates the equalization controller for overcharged and undercharged cells based on the maximum ΔSOC (i.e., difference of the state of charge between the detected cell and the average level) of the cell. If the undercharged cell has a position with more ΔSOC than the overcharged cell or vice versa or other cell, the algorithm executes the controller for equalization of that cell first and then repeats again and again until all cells are equalized within the expected equalization range. This controller algorithm has the flexibility to monitor and equalize both series and parallel cells by using a parallel cell selection switch to the converter. The algorithm

functions as it is to operate the equalization controller. Thus, the equalization controller may protect the battery cells from damage and life reduction, as well as enhance the overall performance of the energy storage system toward an efficient EV drive and sustainable renewable energy applications.

5. Results and Discussion

The proposed charge equalization controller model is implemented with a controller circuit model of 10 series-connected Li-ion battery cells to test and analyze the performance of the model. The model circuit of this charge equalization controller is shown in Figure 1, and the operation of the circuit is described in Sections 2 and 4. The charge controller model, which is an aggregate of individual models an Li-ion battery, power rating, and power converter, is particularized in the preceding section. To find the equalization results, all parts are combined, and each parameter value is configured accordingly. The setup of the charge equalization controller model is arranged with all measurement and display tools to obtain and view the outputs of the equalization controller.

5.1. Bidirectional Flyback DC–DC Converter Output

The equalization controller customs with the bidirectional flyback DC–DC power converter to transfer the power from an overcharged cell to a cell pack or from a cell pack to an overdischarged cell for equalization. To execute the equalization for both charging and discharging battery cells, the bidirectional flyback converter operates as the forward flyback converter for charging overdischarged cells and as the reverse flyback converter for discharging overcharged cells. For cell charging, the forward flyback converter is inputted with a total cell pack voltage of 38.3 V of 10 series-connected cells. The output waveforms of the forward flyback DC–DC converter are shown in Figure 9a. The input of the reverse flyback converter is the overcharge cell voltage of more than 3.85 V based on the ΔSOC (minimum 10%) while discharging the cells, and the excessive charge is distributed to all battery cells. Figure 9b depicts the output of the reverse flyback DC–DC converter.

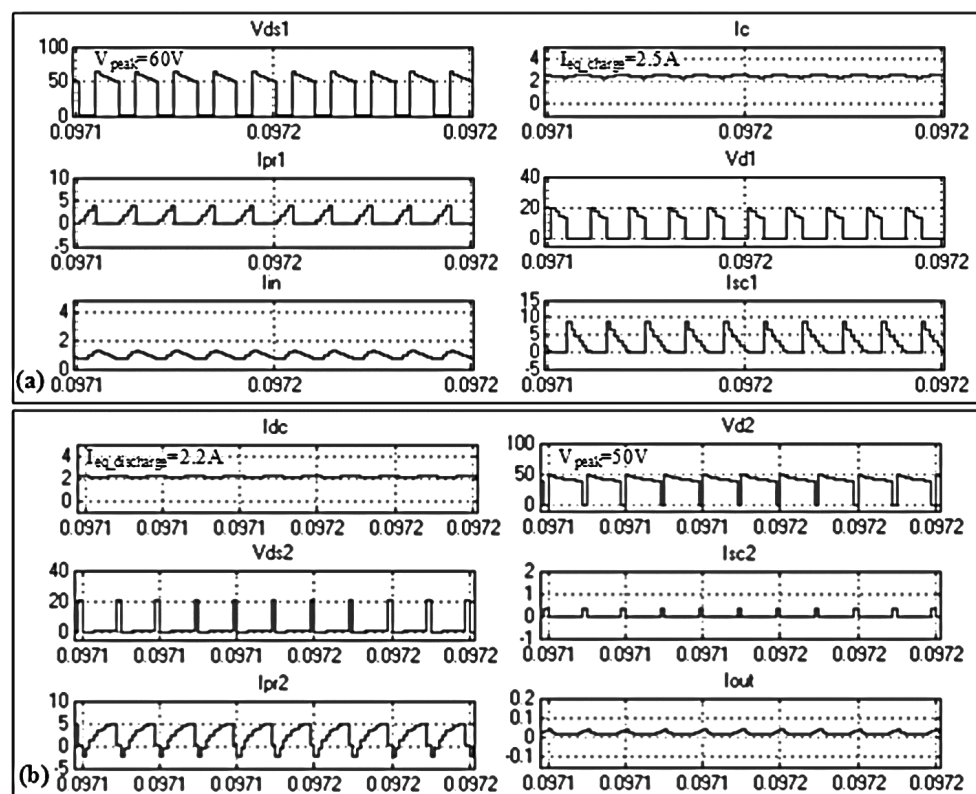


Figure 9. Flyback DC–DC converter outputs during (a) charging and (b) discharging.

As shown in Figure 9, the voltage stresses on the MOSFET switch of the forward converter and on the diode of the reverse converter are 60 and 50 V, respectively, which are in the safe range. The equalization current is maintained at 2.5 A at charge and 2.2 A at discharge. The total power loss and overall efficiency of the converter are obtained using Equations (19) and (20), respectively. The proposed charge equalization controller operates at 91% efficiency.

5.2. Equalization Output

The charge equalization controller model performs for the equalization of 10 series-connected Li-ion battery cells by using the proposed equalization control algorithm. The equalization starts whenever a cell is detected as overcharged or overdischarged on the basis of the normal operating voltage range between 3.72 and 3.88 V, which corresponds to a minimum ΔSOC of 10% for equalization testing. As shown in Figure 10, the operation of the equalization controller is executed by activating the bidirectional cell switch, converter switch, PWM generation, and forward flyback DC–DC converter for the detected battery cell to be charged upholding the requirement of the charge equalization control surface model. The equalization output for an overdischarged cell with different ΔSOC s and an equalization current of 2.5 A is presented in Figure 11a–c. The equalization process continues until ΔSOC reaches a minimum of 2%. In Figure 11a, the cell with 20% ΔSOC reaches the equalization level in 74.4 min, whereas the cells with 15% ΔSOC in Figure 11b and 10% ΔSOC in Figure 11c are equalized in 55.8 and 37.2 min, respectively.

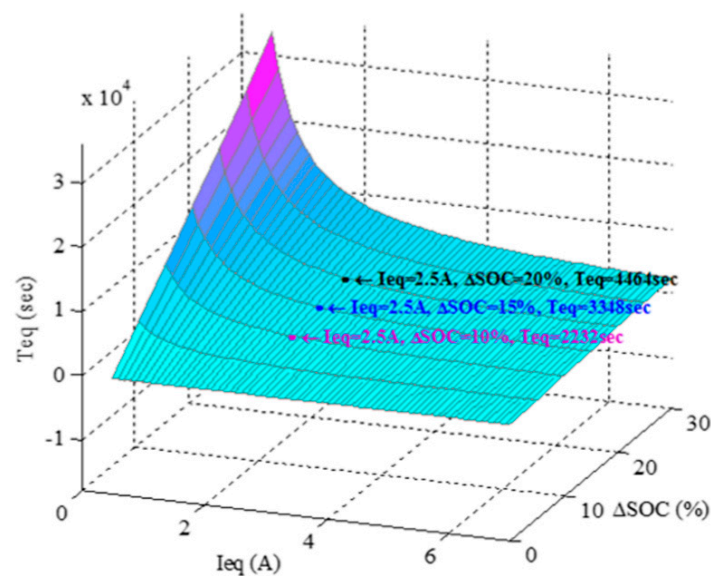


Figure 10. Charge equalization control surface model for battery charge.

As shown in Figure 12, the process of the equalization controller is accomplished by activating the bidirectional cell switch, the converter switch, PWM generation, and the reverse flyback DC–DC converter to discharge the detected battery cell and maintain the condition of the charge equalization control surface model. The equalization output is displayed in Figure 13a–c for an overcharge cell with different ΔSOC s and an equalization current of 2.2 A. The equalization process is continued until the ΔSOC reaches a minimum of 2%. In Figure 13a, the cell with 20% ΔSOC reaches the equalization level in 84.5 min, whereas the cells with 15% ΔSOC in Figure 13b and 10% ΔSOC in Figure 13c are equalized in 63.4 and 42.3 min, respectively. The equalization performances demonstrated for both overdischarged and overcharged cells are proven ideal and timely on the basis of the model values of the charge equalization surface that validates the operation of the proposed charge equalization controller algorithm for series connected lithium-ion battery storage systems.

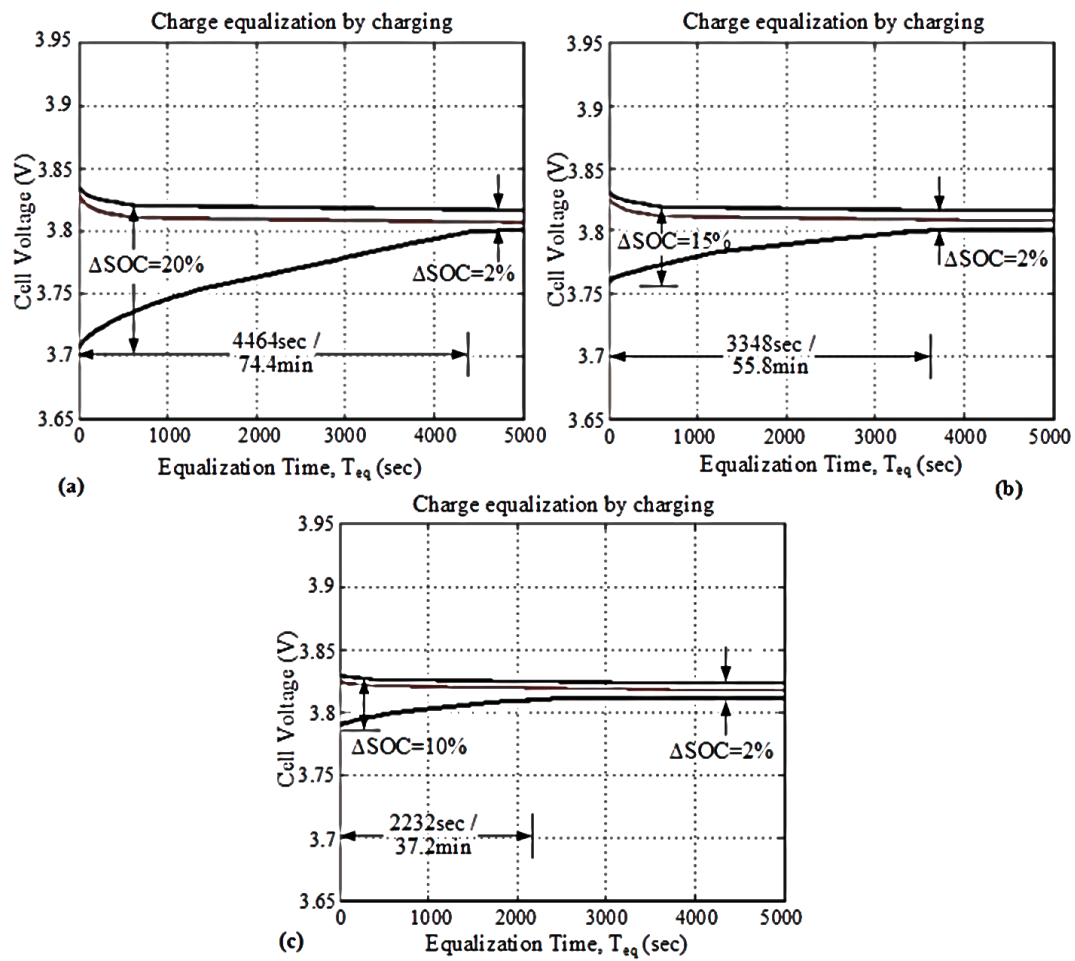


Figure 11. Battery charge equalization performance (a) charge with 20% SOC difference; (b) charge with 15% SOC difference; (c) charge with 10% SOC difference.

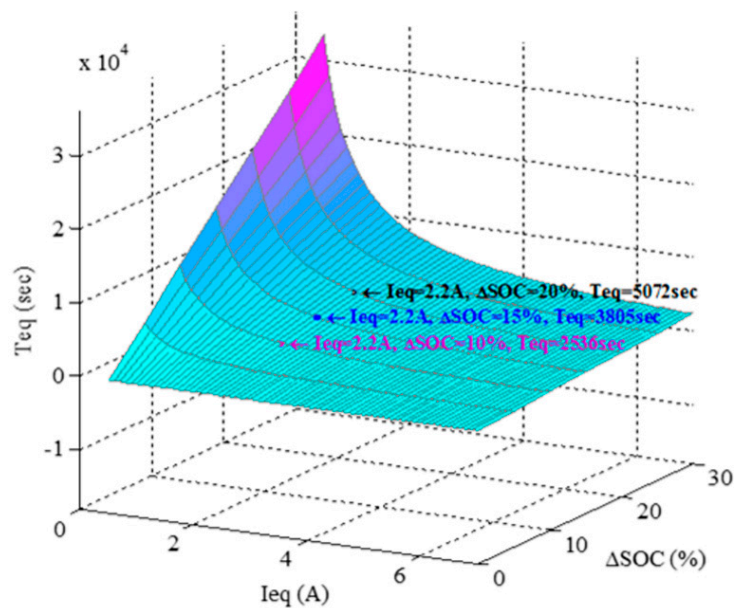


Figure 12. Charge equalization control surface model for battery discharge.

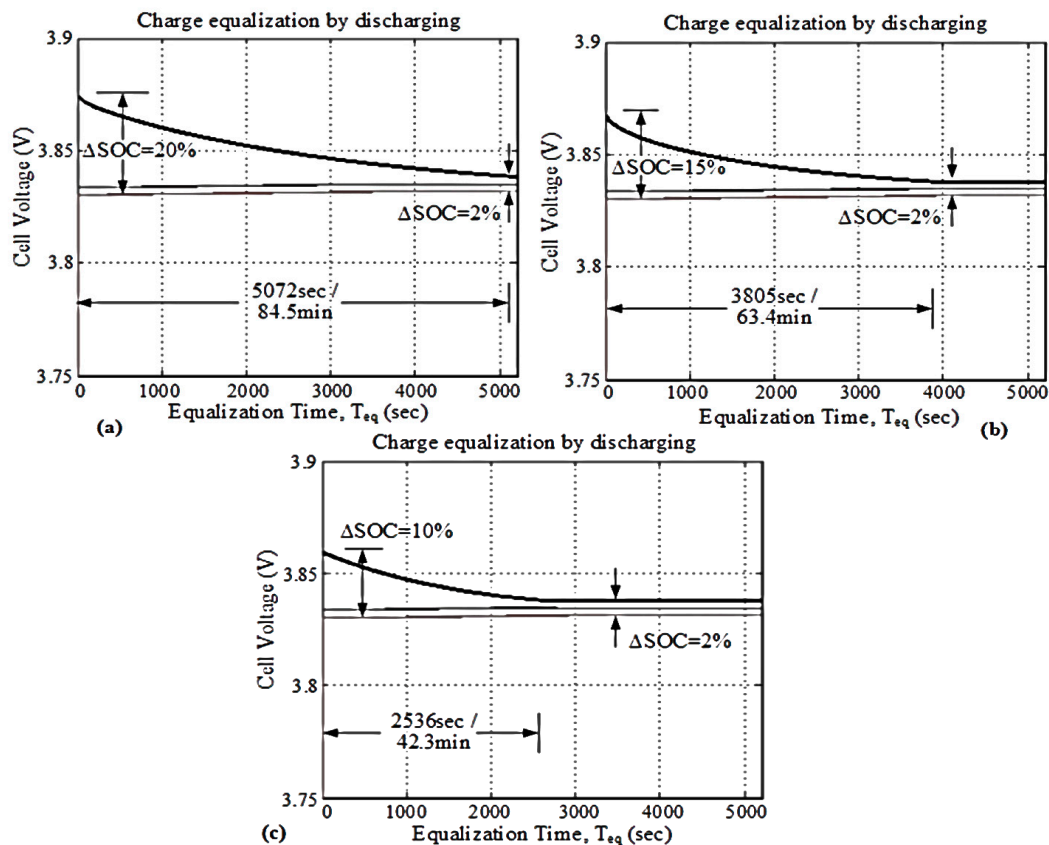


Figure 13. Battery charge equalization performance (a) discharge with 20% SOC difference; (b) discharge with 15% SOC difference; and (c) discharge with 10% SOC difference.

5.3. Performance Comparison of the Proposed Charge Equalization Controller Model

The performance comparison of the proposed charge equalization controller model with the present passive and active equalization controllers is presented in Table 2 [9–22]. The comparison is based on equalization type, equalization speed, execution, control difficulty, and stress. The comparison study also includes the overall efficiency, power loss, size and cost of model design, and required MOSFET switches. Moreover, the evaluation covers the merits and demerits of the charge equalization controller models. The resistor current shunt equalization controller is simple in design and control, and cost effective. Nonetheless, its equalization time and efficiency are poor; it needs thermal management because of high power loss due to heat [12–15]. Conversely, the switched capacitor-based charge equalization controller is simple in implementation and cheap. Nevertheless, its equalization time and efficiency are satisfactory [16,17]. The multi-winding transformer charge equalization controller is good in equalization speed, however, it suffers from high voltage and current stresses, magnetizing losses, an expensive and bulky design, and complex control [18,19]. The flyback converter and resonance converter-based charge equalization controllers are good in equalization and efficiency, but their execution is complex and expensive [10–22]. Meanwhile, the buck–boost converter charge equalization controller features excellent equalization speed and efficiency, but its design is complex, costly, and requires intelligent control [23–25]. The proposed charge equalization controller has excellent equalization speed and less execution trouble. It has comparatively low cost, negligible power loss, and good efficiency. The proposed model is a bidirectional and active equalization controller for both charging and discharging battery cells and can be implemented in modularized design for high power and voltage applications especially in EVs, although it needs closed loop intelligent control.

Table 2. Evaluation of the proposed charge equalization controller model.

Parameters	Resistor Current Shunt [12–15]	Switched Capacitor [16,17]	Multi-Windings Transformer [18,19]	Flyback Converter [20]	Resonant Converter [21,22]	Buck-Boost Converter [23–25]	Proposed
Equalization type	Passive	Active	Active	Active	Active	Active	Active
Equalization time	Satisfactory	Satisfactory	Good	Good	Good	Excellent	Excellent
Execution	Excellent	Very Good	Good	Satisfactory	Satisfactory	Very Good	Excellent
Control	Very Simple	Medium	Medium	Complex	Complex	Complex	Complex
Power loss	High	Low	Low	Low	Negligible	Negligible	Negligible
Cost	Very Cheap	Cheap	Costly	Costly	Costly	Costly	Low-cost
Size	Excellent	Good	Satisfactory	Satisfactory	Satisfactory	Good	Very Good
Voltage/current Stress	Nil/Nil	Low/Low	High/High	Low/Low	High/Low	Low/Low	Low/Low
Switch (for i cells)	i	$2i$	I	i	$2i - 2$	$2i - 2$	$2i + 2$
Transformer	0	0	I	i	0	0	2
Efficiency	Poor	Satisfactory	Good	Good	Good	Good	Very Good
Merits	Cheap, simple, small size	Simple control, bidirectional, low stresses	Good equalization time, bidirectional	Good equalization time, bidirectional	High efficiency	Good equalization time, easy modularized design, bidirectional	Excellent equalization time, bidirectional, easy modularized design, high power application
Demerits	Less efficient, Heat problem, Slow speed, Low power application	Slow Speed	Costly, Complex control, magnetizing loss	Costly, Complex control, magnetizing loss	Costly, voltage stress, difficult to implement	Costly, Complex control	Intelligent control

6. Conclusions

A charge equalization control model for monitoring and equalizing Li-ion battery cells is presented with comprehensive model design and implementation. The model is executed for cell monitoring, cell protection, and charge equalization of 10 series-connected Li-ion battery cells. This paper presents a system overview of the CEC model of Li-ion battery cells and the operating principle with the proposed algorithm, including the modeling of the Li-ion battery, optimal power rating, flyback DC–DC converter, and equalization circuit. The results of the CEC include the outputs of forward and reverse flyback DC–DC converters and the equalization outputs for both charging and discharging Li-ion battery cells. The converter outputs are found in the range of standard equalization current and tolerable stress. The equalization outputs originate at perfect timing as compared with the charge equalization control surface model at ΔSOC values of 20%, 15%, and 10% during the charge and discharge modes of battery cells. The overall outputs reveal that the charge equalization control process with the proposed model is easy to execute, excellent in equalization speed, and efficient with low power loss and stress. The equalization performance demonstrates an accurate balance to maintain the charge levels of battery cells at a minimum of 2% ΔSOC within the operating voltage range. From the comparison validation, it could be stated that this CEC model might be applied as compatible for the monitoring and equalization of series-connected cells for different battery-based energy storage systems. This developed CEC model could be designed for further development of Li-ion battery storage systems of high power and voltage in real time EV and sustainable energy applications.

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